

Fig.1

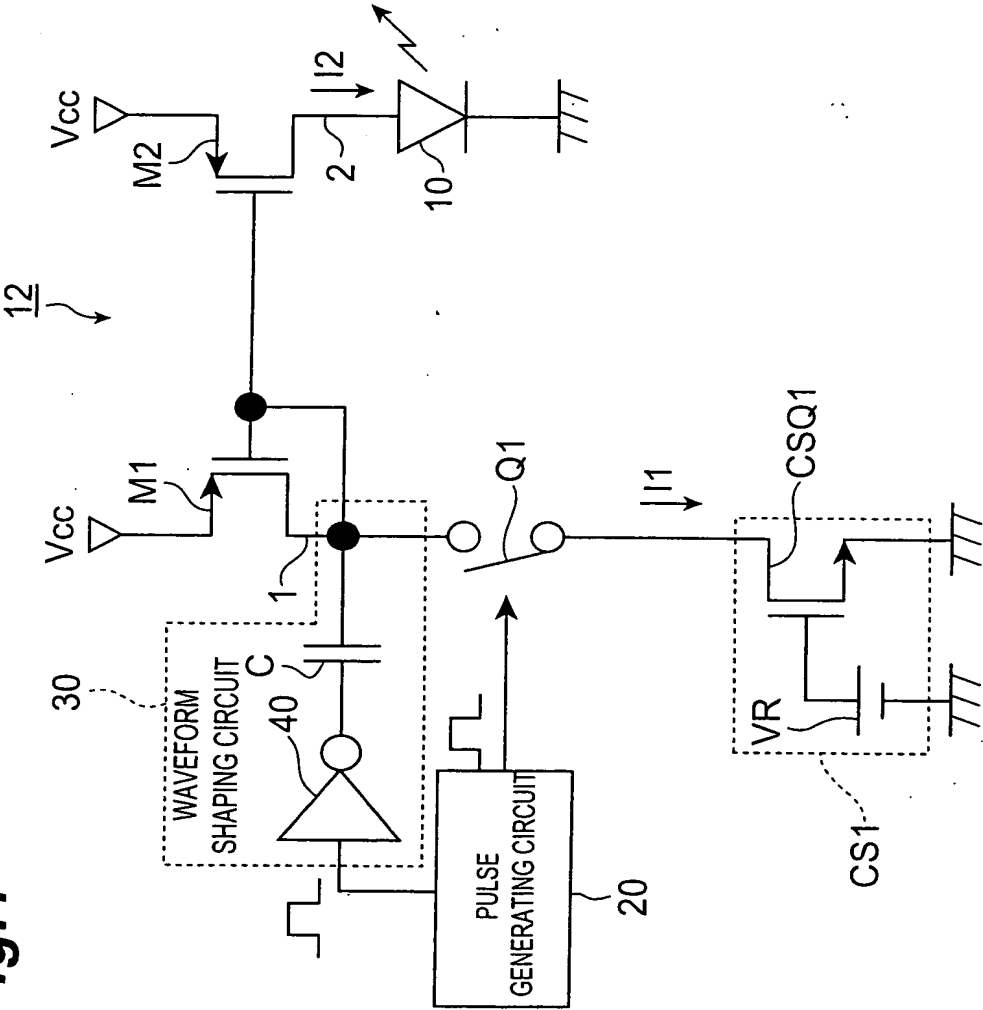


Fig.2A OUTPUT OF PULSE GENERATING CIRCUIT

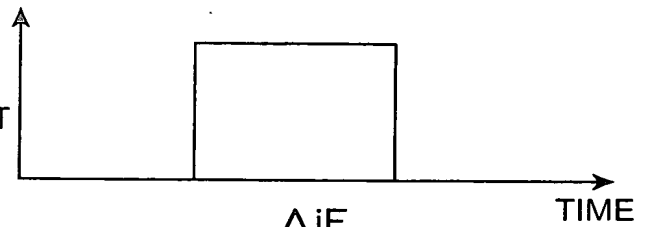


Fig.2B M1, M2 GATE CHARGING CURRENT

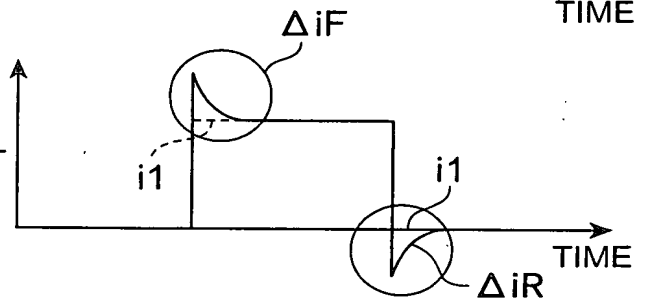


Fig.2C DRIVING CURRENT

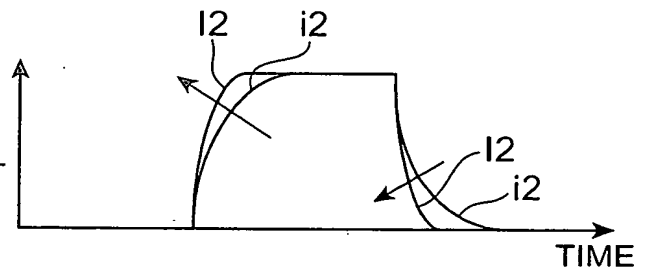


Fig.2D LIGHT OUTPUT

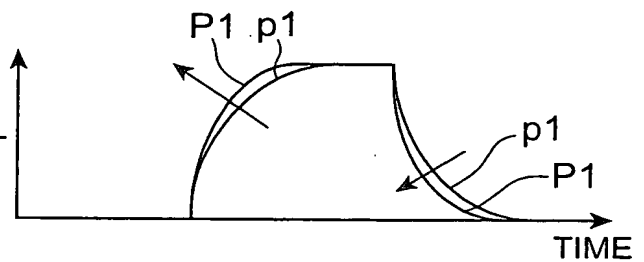


Fig. 3

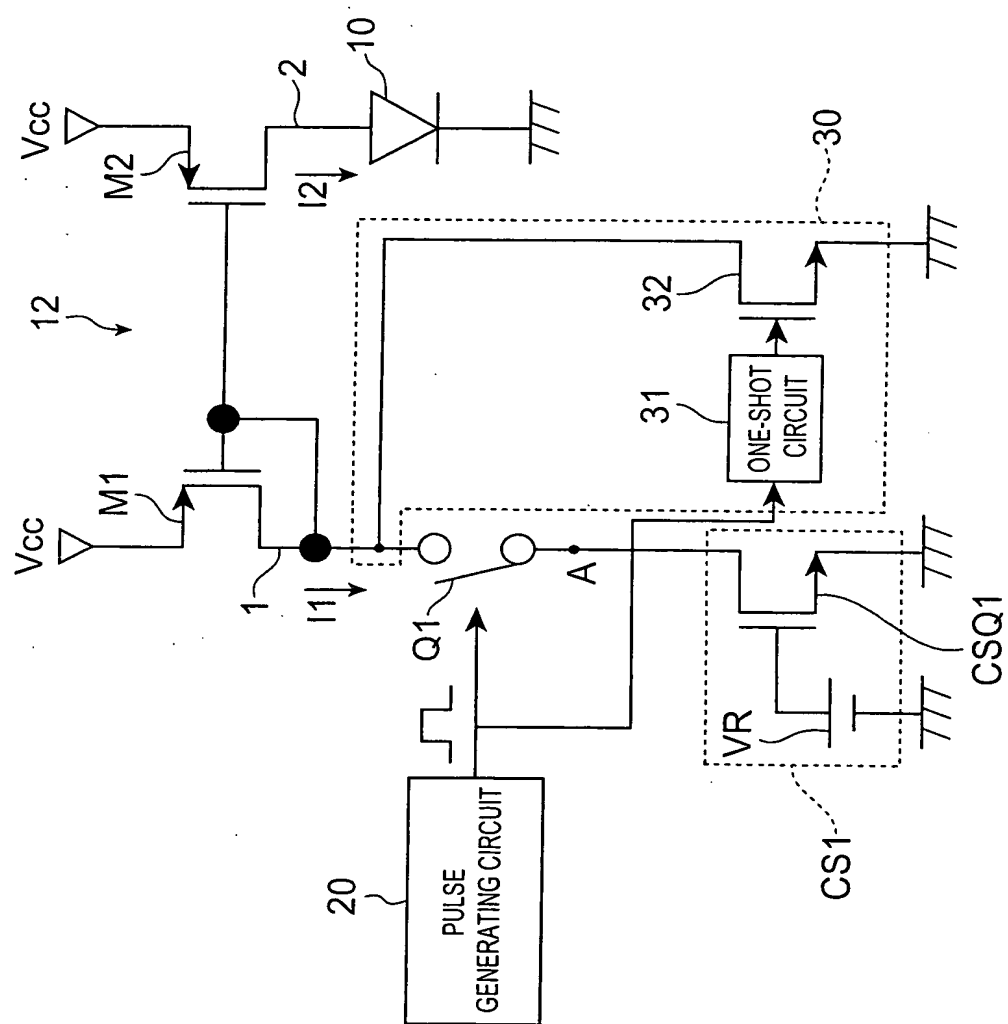


Fig.4A

OUTPUT OF PULSE
GENERATING CIRCUIT

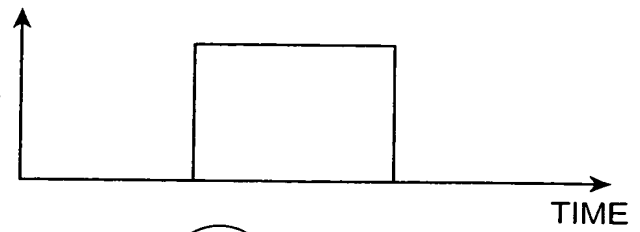


Fig.4B

M1, M2 GATE
CHARGING CURRENT

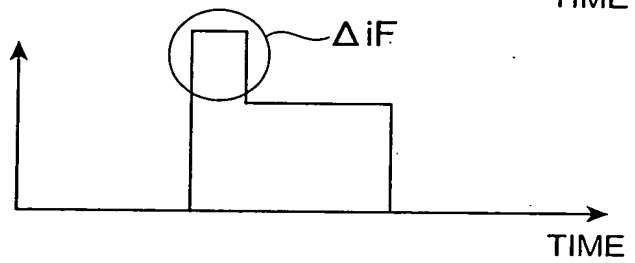


Fig.4C

DRIVING
CURRENT

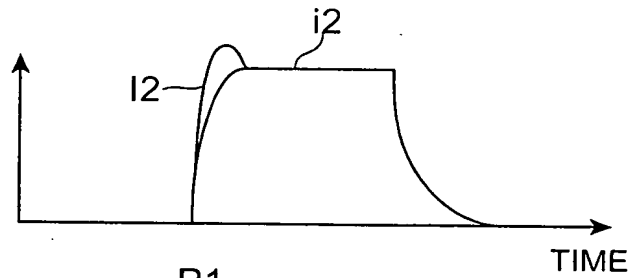
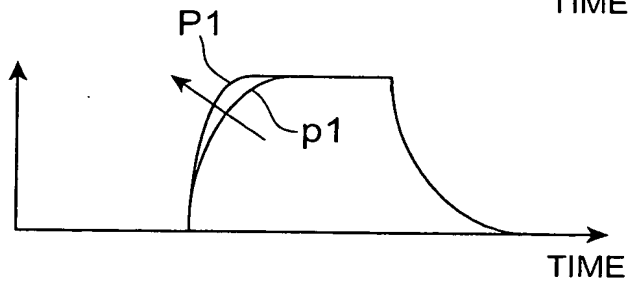


Fig.4D

LIGHT OUTPUT



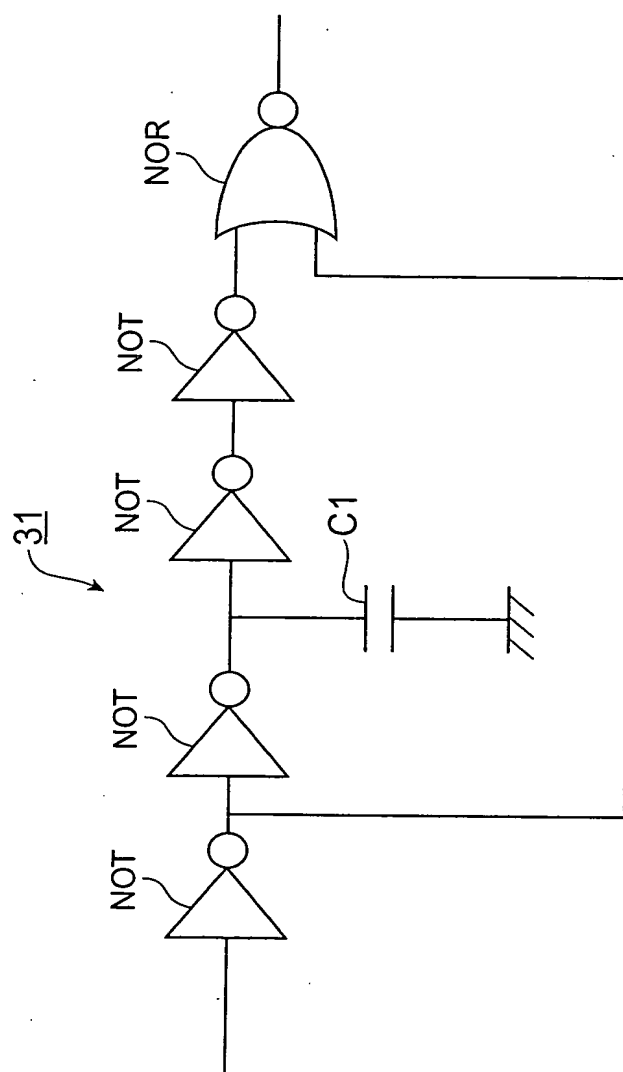


Fig. 5

Fig.7A

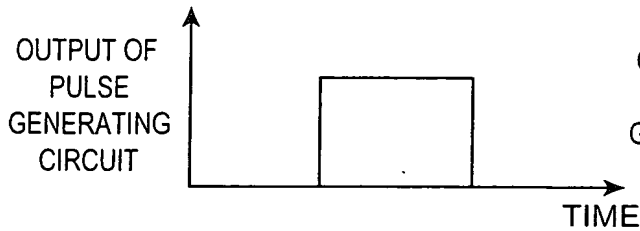


Fig.7E

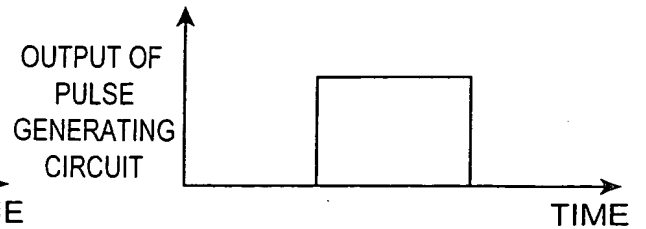


Fig.7B

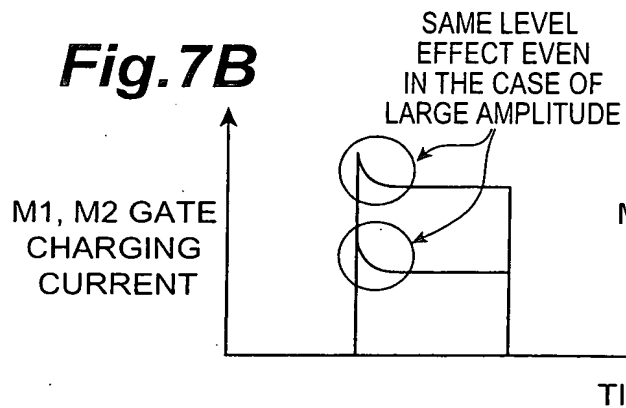


Fig.7F

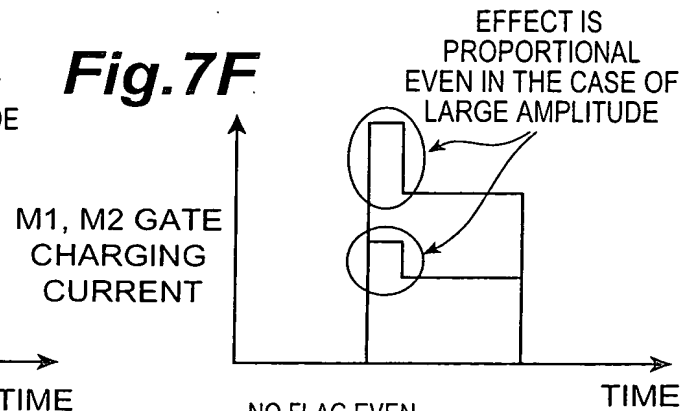


Fig.7C

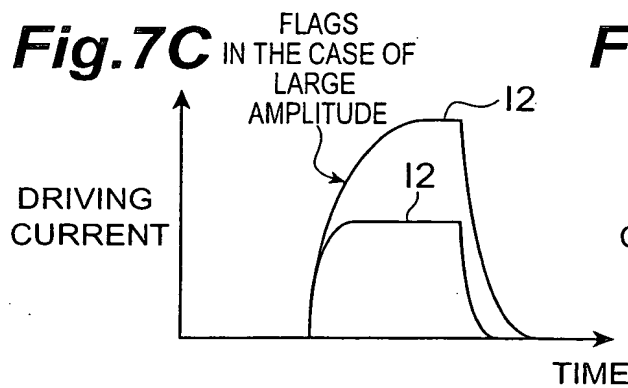


Fig.7G

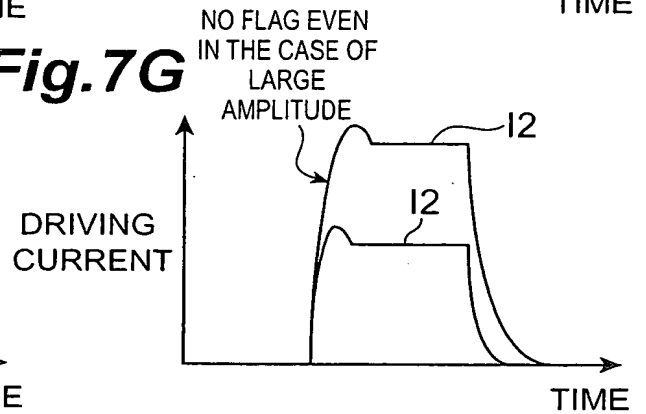


Fig.7D

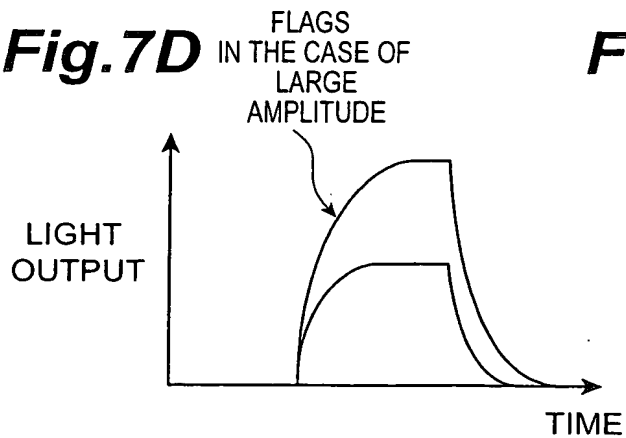


Fig.7H

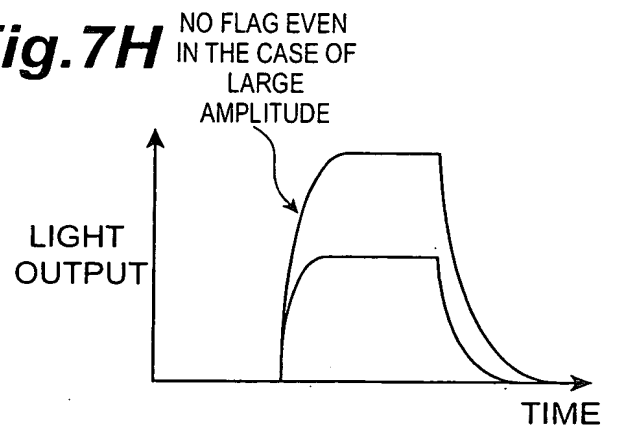


Fig.8

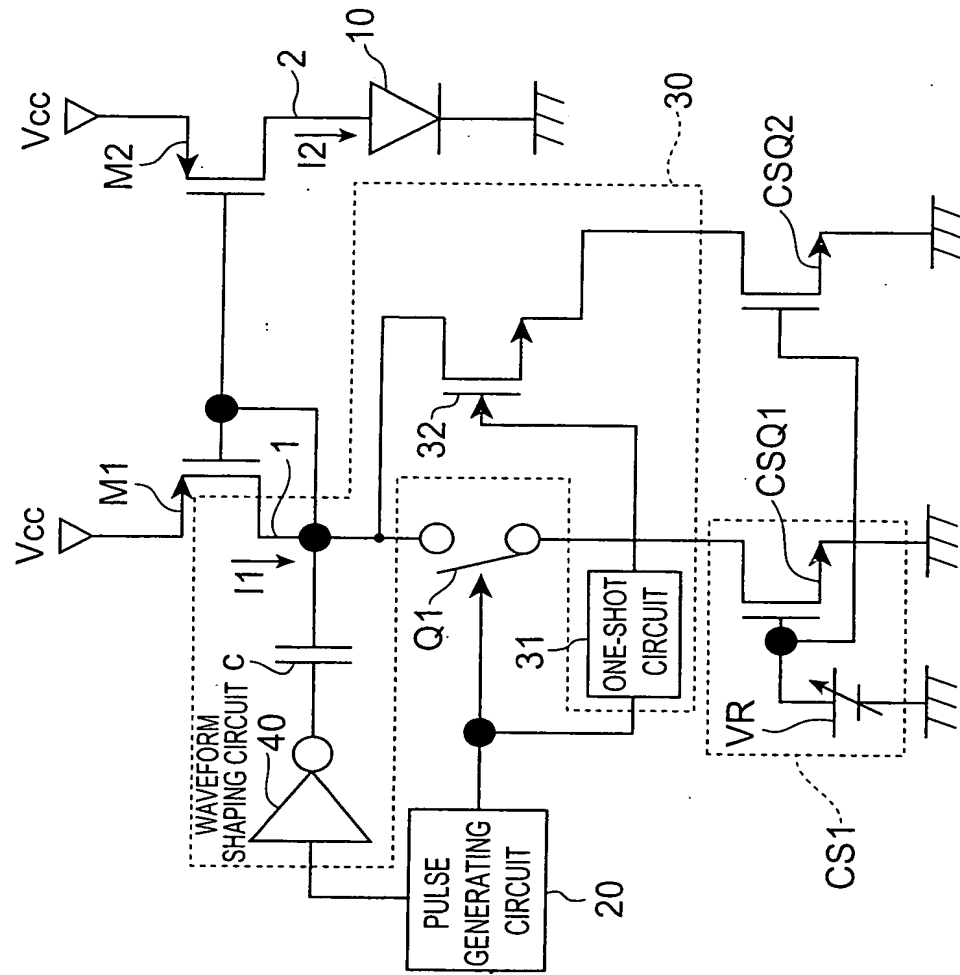


Fig.9A

OUTPUT OF PULSE
GENERATING CIRCUIT

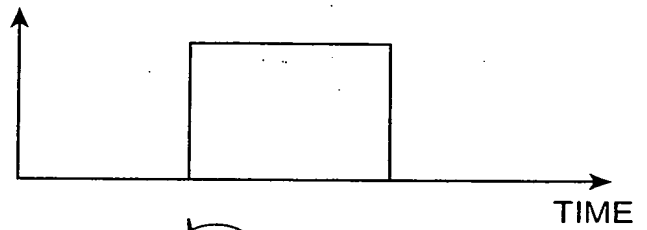


Fig.9B

M1, M2 GATE
CHARGING CURRENT

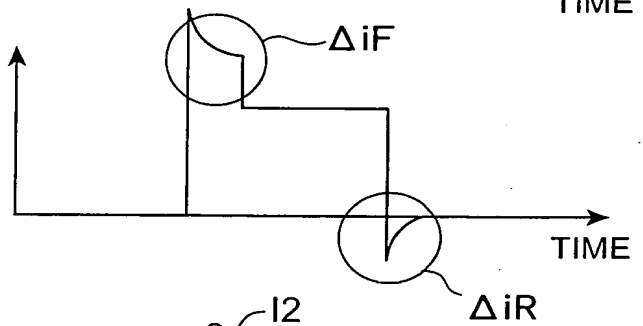


Fig.9C

DRIVING CURRENT

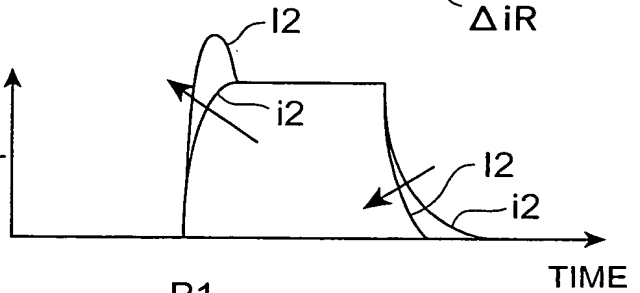
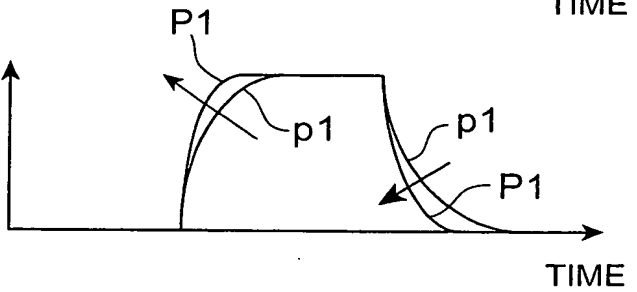


Fig.9D

LIGHT OUTPUT



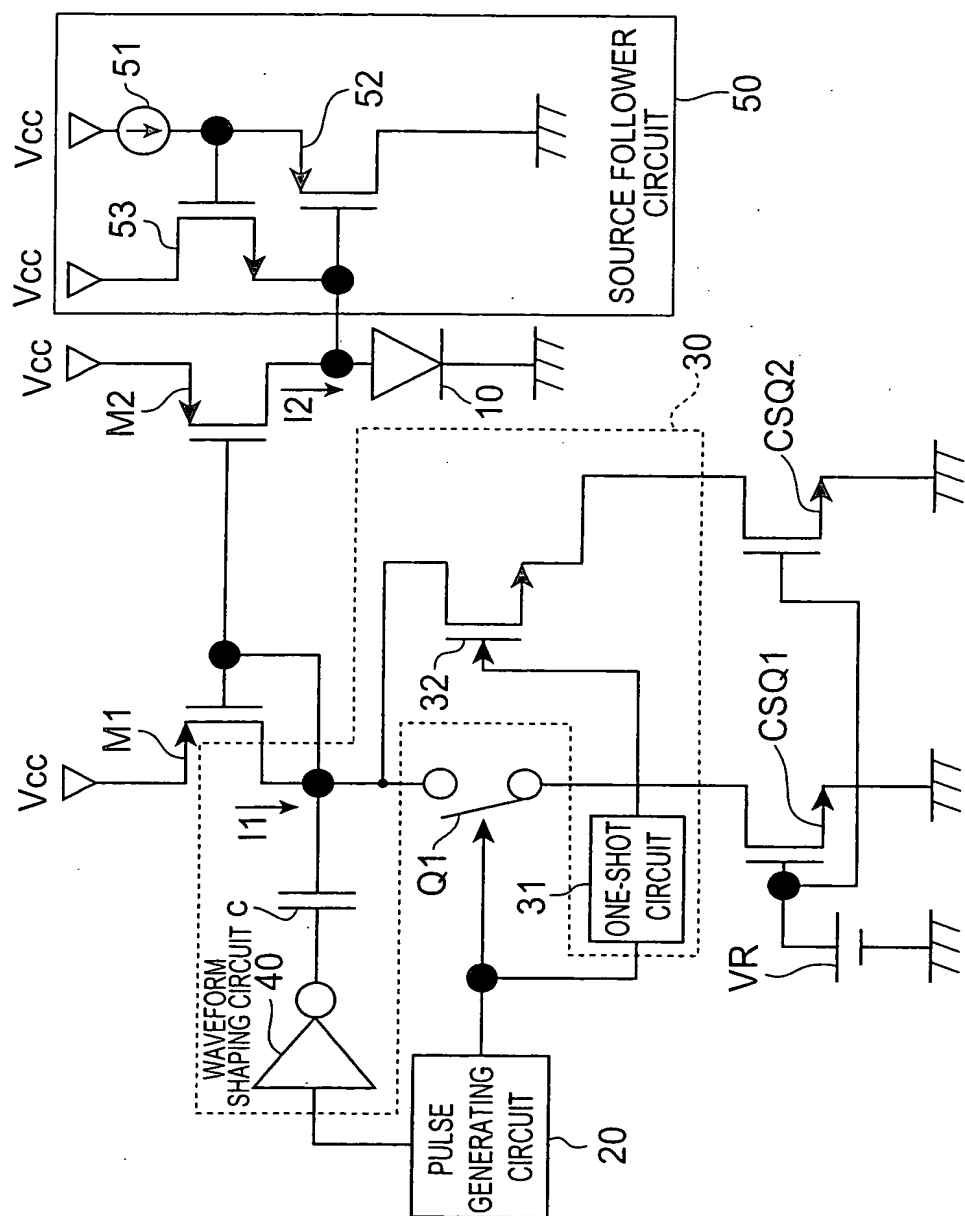


Fig. 10

Fig.11A

DRIVING CURRENT

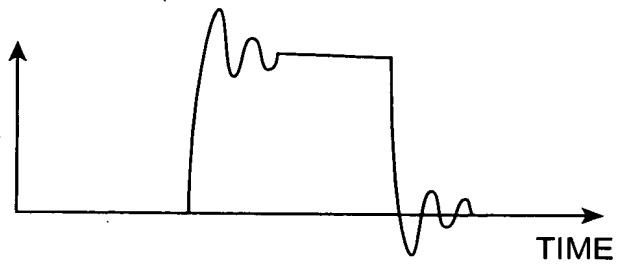


Fig.11B

LIGHT OUTPUT

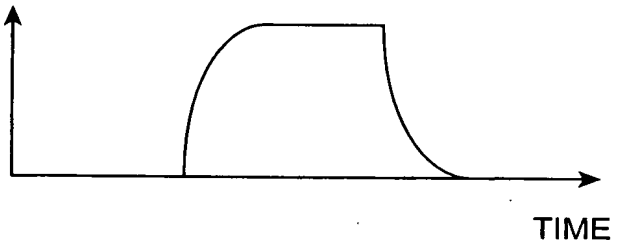
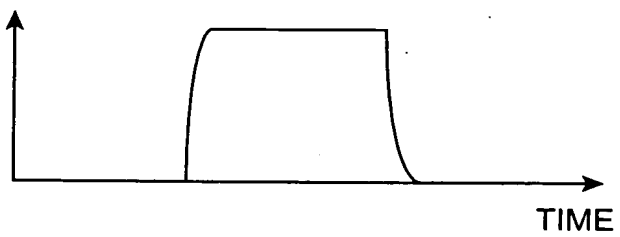


Fig.11C

LIGHT OUTPUT



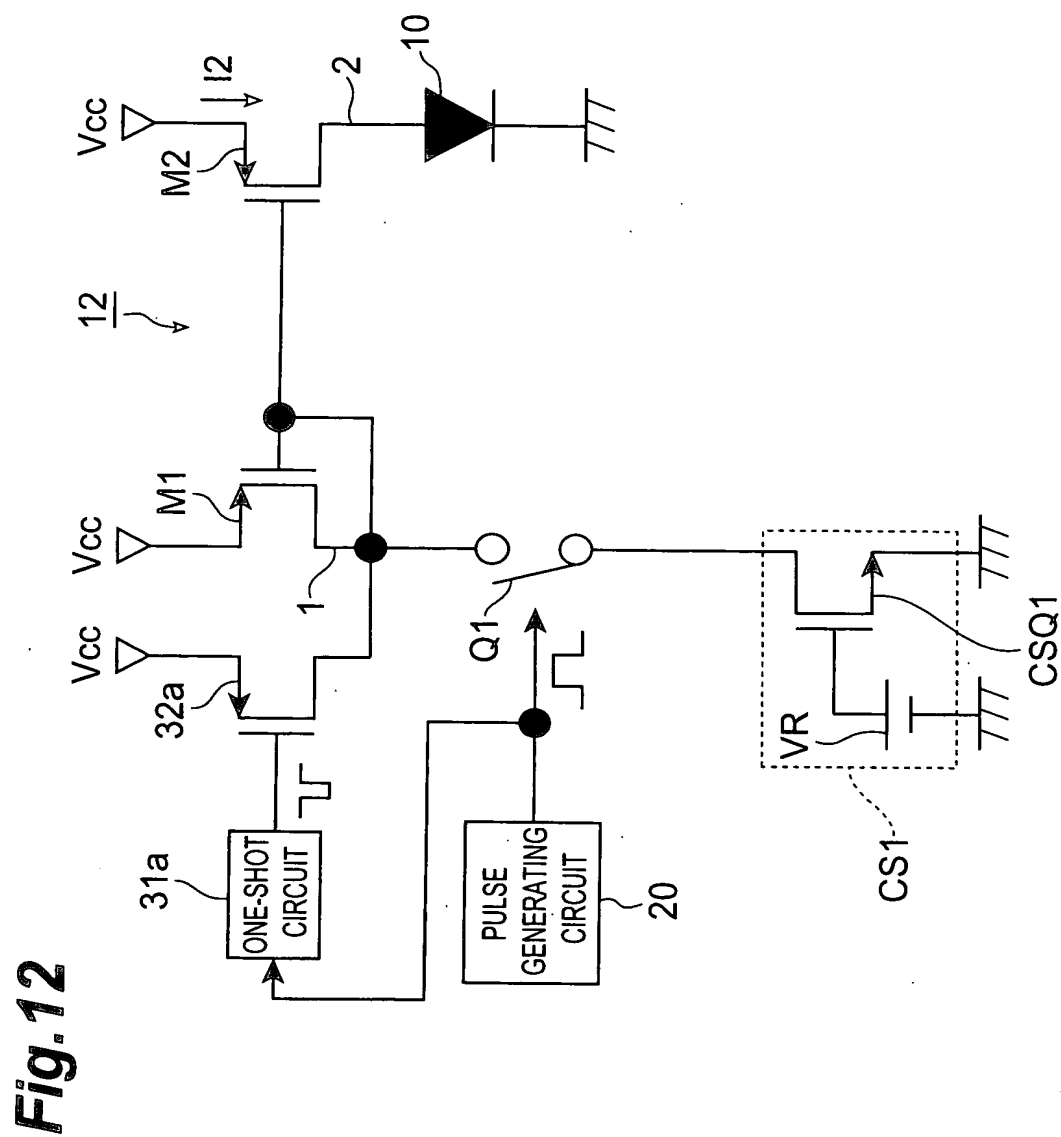


Fig.13

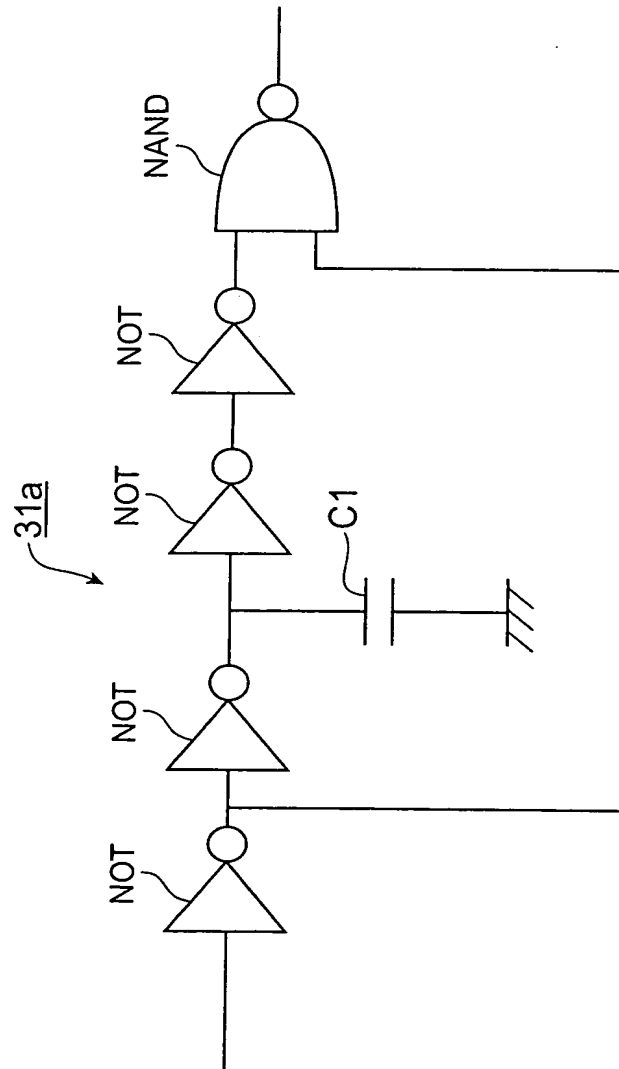


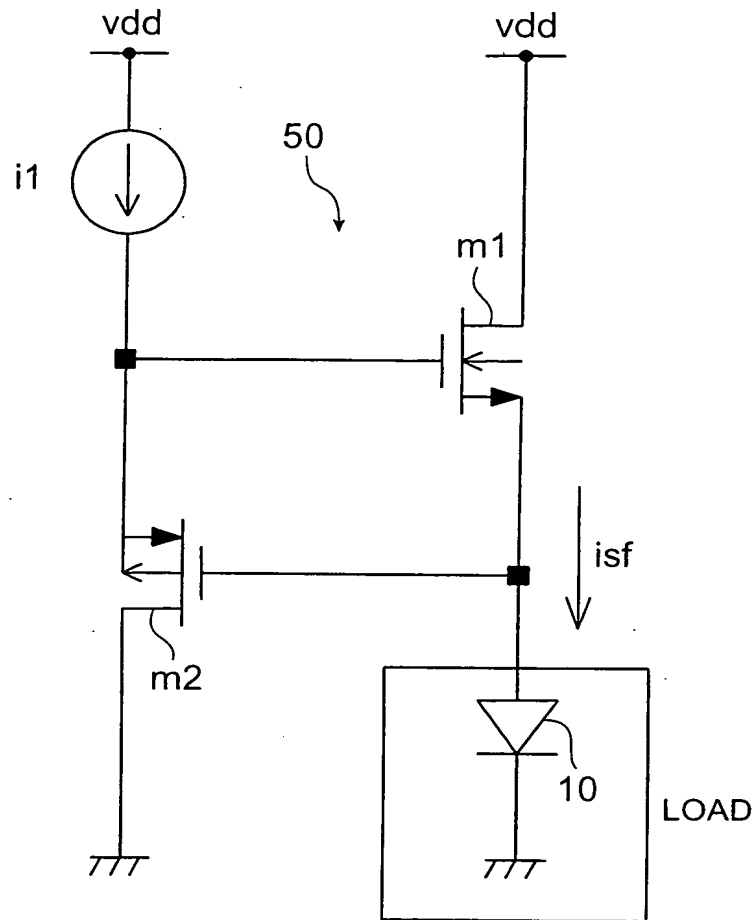
Fig.14

Fig.15

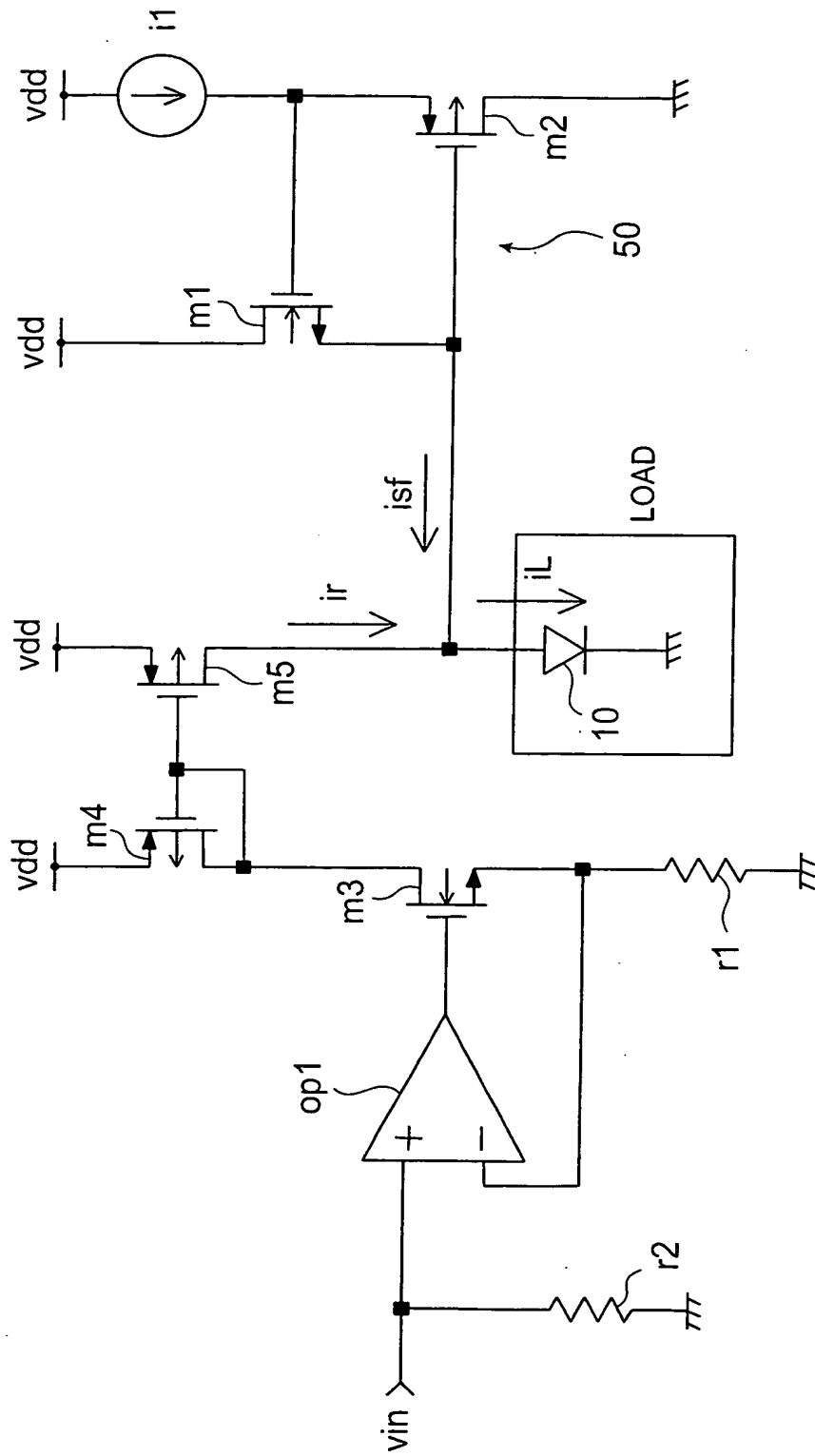
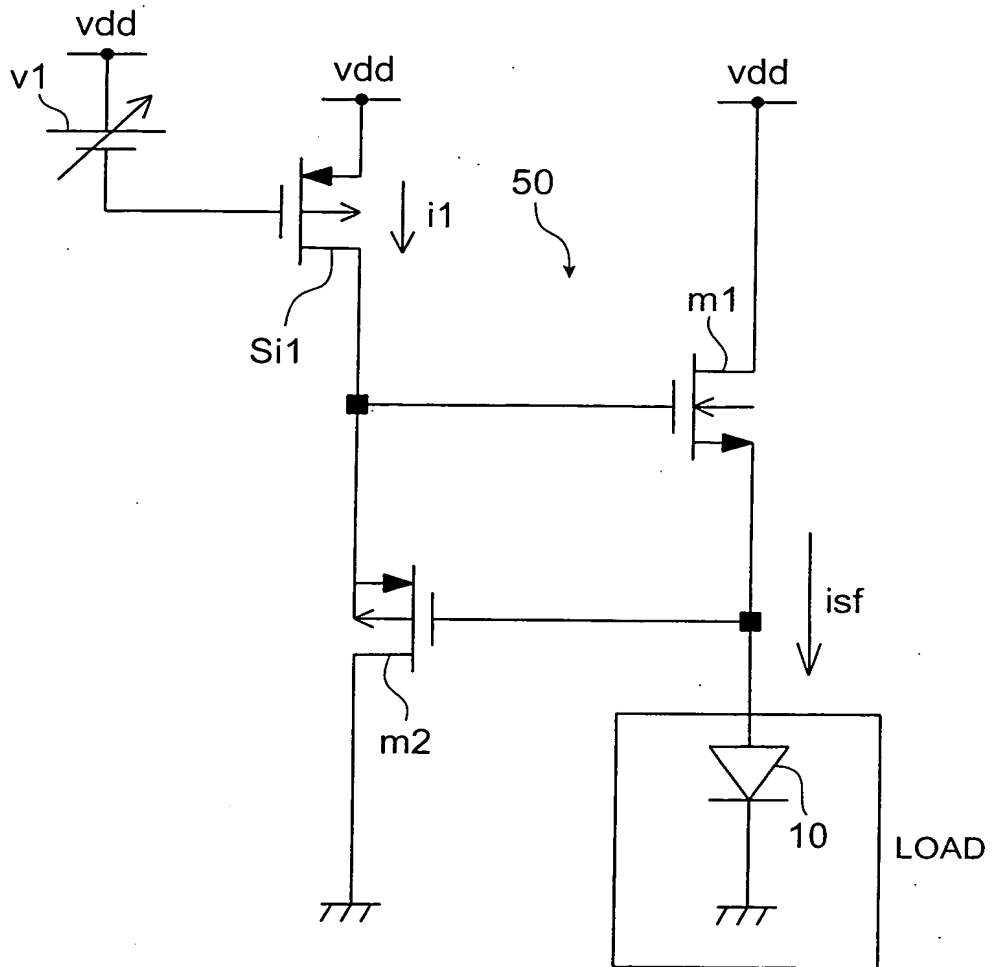


Fig.16

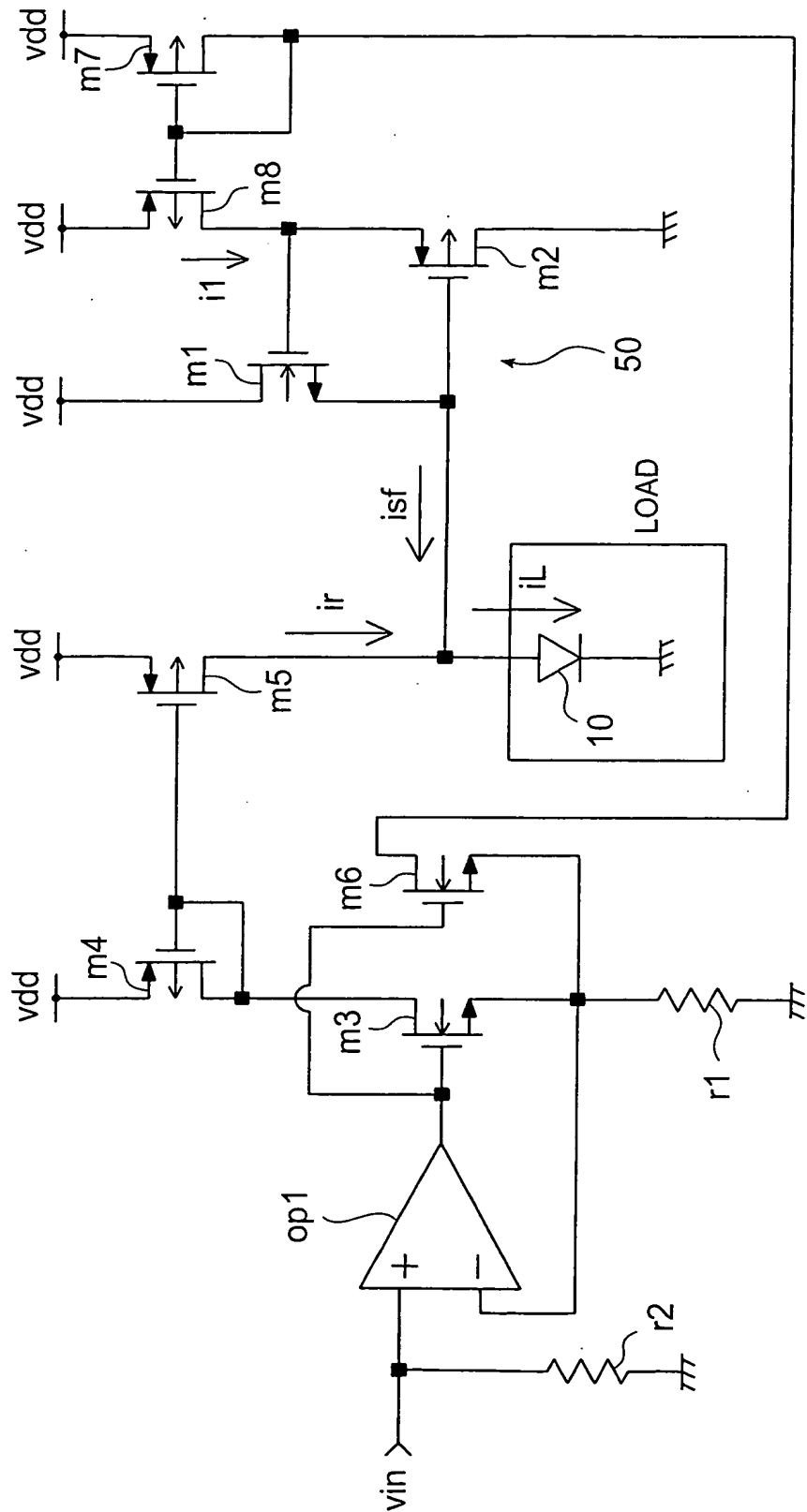


Fig.17

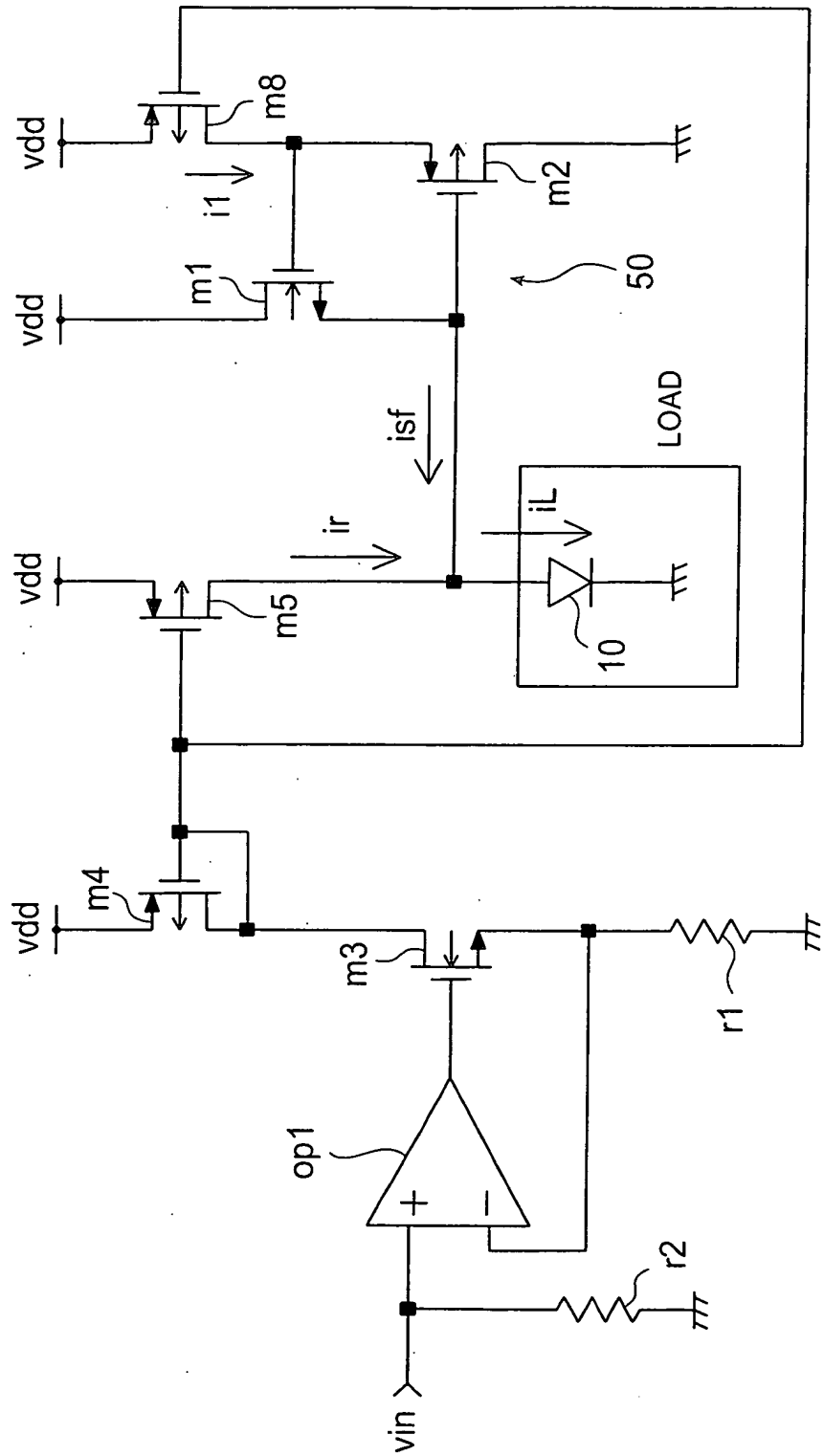


Fig.18

Fig.19A

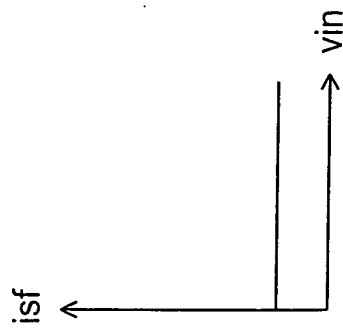


Fig.19B

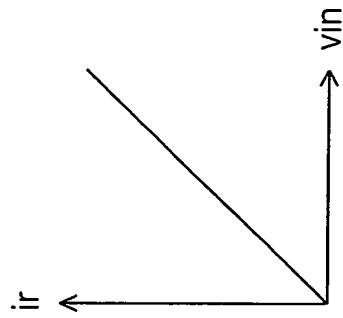


Fig.19D

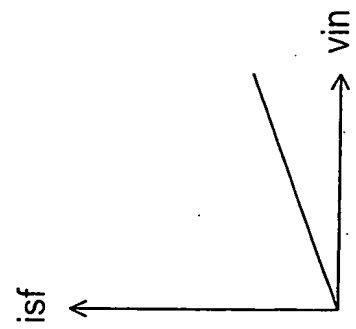


Fig.19E

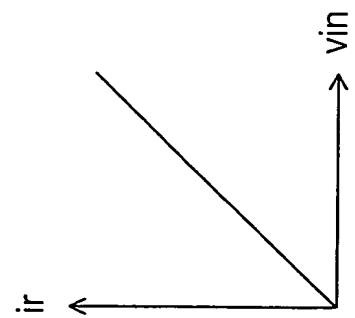


Fig.19C

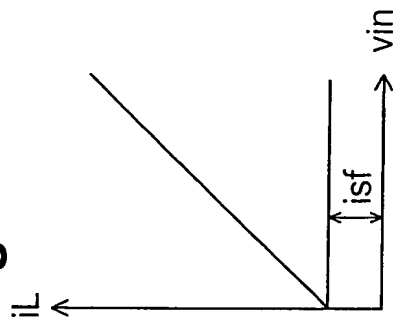


Fig.19F

